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Docket No. OSP-15515

AMENDMENTS TO THE CLAIMS:

1-4. (Canceled)

5. (Currently amended) A liquid crystal display device, comprising:

pixels equipped with a liquid crystal cell and a switch element, which are arranged at positions where scan lines and data lines intersect;

a data line drive circuit for supplying from said data line and said switch element to said liquid crystal cell a write signal corresponding with image data;

a control circuit for inverting a polarity of said write signal after every plurality of scan lines; and

a scan line drive circuit for supplying a drive signal to said scan lines and switching said switch elements ON and OFF,

wherein said scan line drive circuit ~~and said data line drive circuit~~, in the scan lines where the polarity of said write signal is inverted, supply said drive signal ~~and said write signal~~ for a period of time that is longer than one horizontal period by a fixed amount of time that is determined within a range of an invalid period, during which said invalid period said image data is not supplied, and in following scan lines to which is supplied a write signal of the same polarity as said scan line, supply said drive signal ~~and said write signal~~ for a period of time shorter than one horizontal period by said fixed amount of time.

6. (Original) A liquid crystal display device according to claim 5, wherein said scan line drive circuit adjusts a period for which said drive signal is supplied, in accordance with an output enable signal for controlling whether or not to supply said drive signal to said scan

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line.

7. (Previously presented) A liquid crystal display device according to claim 5, further comprising:

a drain line precharging circuit to precharge drain lines for said write signal as a preliminary operation during those scan lines when the polarity of said write signal is first inverted.

8. (Previously presented) A liquid crystal display device according to claim 5, wherein said scan line drive circuit provides an output enable signal for controlling whether or not to supply said drive signal to said scan line.

9. (Previously presented) A liquid crystal display device according to claim 5, wherein said shorter period is achieved by a correction to a clock signal used to activate gate lines of said switching elements in a scan line.

10. (Previously presented) A liquid crystal display device according to claim 5, wherein said scan line drive circuit receives an input representing an adjustment for a time delay, said adjustment being selected to reduce a difference of brightness between scan lines.

11. (Currently amended) A method to reduce a difference in a brightness between scan lines in a liquid crystal display device having a polarity inversion of write signals after a predetermined plurality of said scan lines, said method comprising:

providing a timing adjustment as an input into a scan line drive circuit that provides a drive signal to said scan lines for switching switch elements to first and second positions,

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wherein said scan line drive circuit, in the scan lines where the polarity of said write signal is inverted, supplies said drive signal for a period of time that is longer than one horizontal period by a fixed amount of time that is determined within a range of an invalid period, during which said invalid period said image data is not supplied, and in following scan lines to which is supplied a write signal of the same polarity as said scan line, supplies said drive signal and ~~said write signal~~ for a period of time shorter than one horizontal period by said fixed amount of time.

12. (Previously presented) The method of claim 11, wherein said liquid crystal display device precharges drain lines for said write signal as a preliminary operation during those scan lines when the polarity of said write signal is first inverted.

13. (Previously presented) The method of claim 11, wherein said drive signal provides an output enable signal for controlling whether or not to supply said drive signal to said scan line.

14. (Previously presented) The method of claim 11, wherein said shorter period is achieved by a correction to a clock signal used to activate gate lines of said switching elements in a scan line.

15. (Currently amended) A liquid crystal display device, comprising:

pixels equipped with a liquid crystal cell and a switch element, which are arranged at positions where scan lines and data lines intersect;

a data line drive circuit for supplying from said data line and said switch element to said liquid crystal cell a write signal corresponding with image data;

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a control circuit for inverting a polarity of said write signal after every plurality of scan lines; and

means for supplying a drive signal to said scan lines and switching said switch elements to first and second positions,

wherein said means for supplying a drive signal to said scan lines, in the scan lines where the polarity of said write signal is inverted, supplies said drive signal and ~~said write~~ signal for a period of time that is longer than one horizontal period by a fixed amount of time that is determined within a range of an invalid period, during which said invalid period said image data is not supplied, and in following scan lines to which is supplied a write signal of the same polarity as said scan line, supplies said drive signal and ~~said write~~ signal for a period of time shorter than one horizontal period by said fixed amount of time.

16. (Previously presented) The liquid crystal device of claim 15, wherein said means for supplying a drive signal comprises an output enable signal for controlling whether or not to supply said drive signal to said scan line.

17. (Previously Presented) The liquid crystal device of claim 15, wherein said means for supplying a drive signal comprises a correction to a clock signal used to activate gate lines of said switching elements in a scan line.